



2814

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Kusumoto, et al.

Art Unit : 2814

Serial No. : 09/941,367

Examiner : Theresa T. Doan

Filed : August 28, 2001

Title : METHOD FOR PRODUCING INSULATED GATE THIN FILM  
SEMICONDUCTOR DEVICE#14/EXT. (2)  
7/12/03  
C. P. Pai

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

TRANSMITTAL LETTER AND PETITION FOR AUTOMATIC EXTENSION

Correspondence relating to this application is enclosed. The required fees are computed below. Please apply any charges not covered, or any credits, to Deposit Account No. 06-1050.

Total Claims	60	-	108	=	0	\$0
Independent	23	-	23	=	0	\$0
First Presentation of Multiple Dependent Claims						\$0

Applicant hereby petitions under 37 C.F.R. §1.136 for a 2 month extension of time.

\$410

TOTAL FEE DUE

\$410

A check for \$410 is attached.

Respectfully submitted,

Date:

6/23/2003

  
Joseph F. Key  
Reg. No. 44,827

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